

FORM LD-6

REQUEST FOR CORRECTION OF CLERICAL ERROR OR FOR AMENDMENT

[Sections 8(3), 12 and 31 Rules 28, 62, 68]

In matters of _____ I / we _____ being the²
applicant(s) in the above matter hereby
opponent(s)
registered proprietor(s)
registered user(s)
request that _____

³A copy of the request has been served on the registered proprietor(s) / registered user(s).

Dated this _____ day of _____ 20____.

To

The Registrar of the Semiconductor Integrated Circuits Layout-Design, Office of the Semiconductor Integrated Circuits
Layout-Design Registry at⁵ _____

1. Insert words and reference number identifying the entry or application or opposition.
2. Strike out words which are not applicable.
3. Strike out if not applicable.
4. Signature.
5. State the name of the place of the appropriate office of the Semiconductor Integrated Circuits Layout-Design Registry. See Rule 4.