

FORM LD-2

NOTICE OF OPPOSITION TO APPLICATION FOR REGISTRATION OF A LAYOUT-DESIGN

[Section 11(1), Rule 33]

(To be filled in triplicate)

In the matter of Application No. _____ by _____ I (we)¹ _____ hereby give notice of my (our) intention to oppose the registration of the semiconductor integrated circuit layout-design advertised under the above number in the semiconductor integrated circuits layout-design Journal dated the _____ day of _____ 20____, No. _____ page _____.

The grounds of opposition are as follows^{2, 3}:

All communication in relation to these proceedings may be sent to the following address in India⁴:

Dated this _____ day of _____ 20____

⁵ _____

To

The Registrar of Semiconductor Integrated Circuits Layout-Design Office of the Semiconductor Integrated Circuits Layout-Design Registry at⁶ —.

1. State full name and address. An address for service in India should be given if the opponent has no place of business or residence in India
2. Give proof/details if opposition is on the ground that the layout-design applied-for registration is not original.
3. Give proof if the opposition is on the ground that the layout-design has been commercially exploited for a period more than two years on the date on which the application is applied for registration.
4. To be stated only by an opponent who has given the address of his principal place of business or of residence in India but who desires to give for the purposes of the opposition proceedings address in India different from that address.
5. Signature of the applicant or his agent (legal practitioner or registered layout-design agent or person in the sole and regular employment of the applicant. See section 84.)
6. State the name of the place of the appropriate office of the Semiconductor Integrated Circuits Layout-Design Registry.