

FORM LD-28

**OPPOSITION UNDER SECTION 40(3) OF THE SEMICONDUCTOR INTEGRATED CIRCUITS LAYOUT-
DESIGN ACT, 2000 BEFORE THE LAYOUT-DESIGN APPELLATE BOARD**

[Section 40(3) Rule 73(2)]

Particulars of the application against which the application is filed _____

Name and address of the applicant _____

Name and address of the opposite party _____

Grounds of opposition _____

Signature of opposite party

To

The Layout-Design Appellate Board