

FORM LD-30

APPLICATION FOR EXTENSION OF TIME FOR GIVING NOTICE OF OPPOSITION

[section 11(1) rule 33(2)]

In the matter of Application No. _____

I/we¹ _____ hereby apply for extension of time² _____ for giving notice of opposition to the registration of the semiconductor integrated circuit layout-design advertised under the above number in the Semiconductor Integrated Circuits Layout-Design Journal dated the _____ day of _____ 20____.

The reasons for making this application are as follow :—

All communication relating to this application may be sent to the following address in India³:—

Dated this _____ day of _____ 20____.

Signature _____

To

The Registrar of Semiconductor Integrated Circuits Layout-Design Registry.

The Office of the Semiconductor Integrated Circuits Layout-Design Registry at⁴ _____.

- _____
1. State the full name and address.
 2. Insert the period of extension required which shall not exceed one month beyond three months from the date of advertisement or re-advertisement, as the case may be, of the application in the Semiconductor Integrated Circuits Layout-Design Journal.
 3. To be stated if the address given is not the address in India.
 4. State the name of the place of the appropriate office of the Semiconductor Integrated Circuits Layout-Design Registry. See rule 4.