

FORM LD-11

**JOINT REQUEST BY REGISTERED PROPRIETOR AND TRANSFeree TO REGISTER THE TRANSFeree
AS SUBSEQUENT PROPRIETOR OF LAYOUT-DESIGN UPON THE SAME DEVOLUTION OF TITLE**

(Section 23 Rule 47)

We¹ _____ and² _____ hereby request, under rule 47, that the name of³ _____ carrying on business as⁴ _____ at⁵ _____ may be entered in the register of layout-design as proprietor of the layout-design no. _____ as from the⁶ _____ by virtue of⁷ _____ of which the original and an attested copy are enclosed herewith.

The assignment of the layout-design was⁸ (not) made otherwise than in connection with the goodwill of the business in which the layout-design (has been)/was used⁸ (and there is sent herewith copy of the Registrar's direction to advertise the assignment, a copy of each of the advertisement, complying therewith and a statement of the dates of the issue of any publications containing them).

We declare that the facts and matters stated herein are true to the best of our knowledge, information and belief.

Dated this _____ day of _____ 20__.

(Signature of the applicants)

To

The Registrar of Semiconductor Integrated Circuits Layout-Design,

The Office of the Semiconductor Integrated Circuits Layout-Design Registry at⁹ _____

1. Full name, address, and nationality of registered proprietor, or other assignor or transmitter.
2. Full name, address and nationality of transferee.
3. Name of transferee.
4. Description of transferee (calling or profession).
5. Address of the principal place of business in India, if any, of the transferee. If there is no place of business in India state the address of the place of residence in India. If there is not even place of residence in India, state the address in the home country abroad and an address for service in India.
6. Date of acquisition of proprietorship.
7. Full particulars of the instrument of assignment or transmission, if any, or statement of case.
8. Strike out words not applicable.
9. State the name of the place of the appropriate office of the Semiconductor Integrated Circuits Layout-Design Registry. See rule 4.